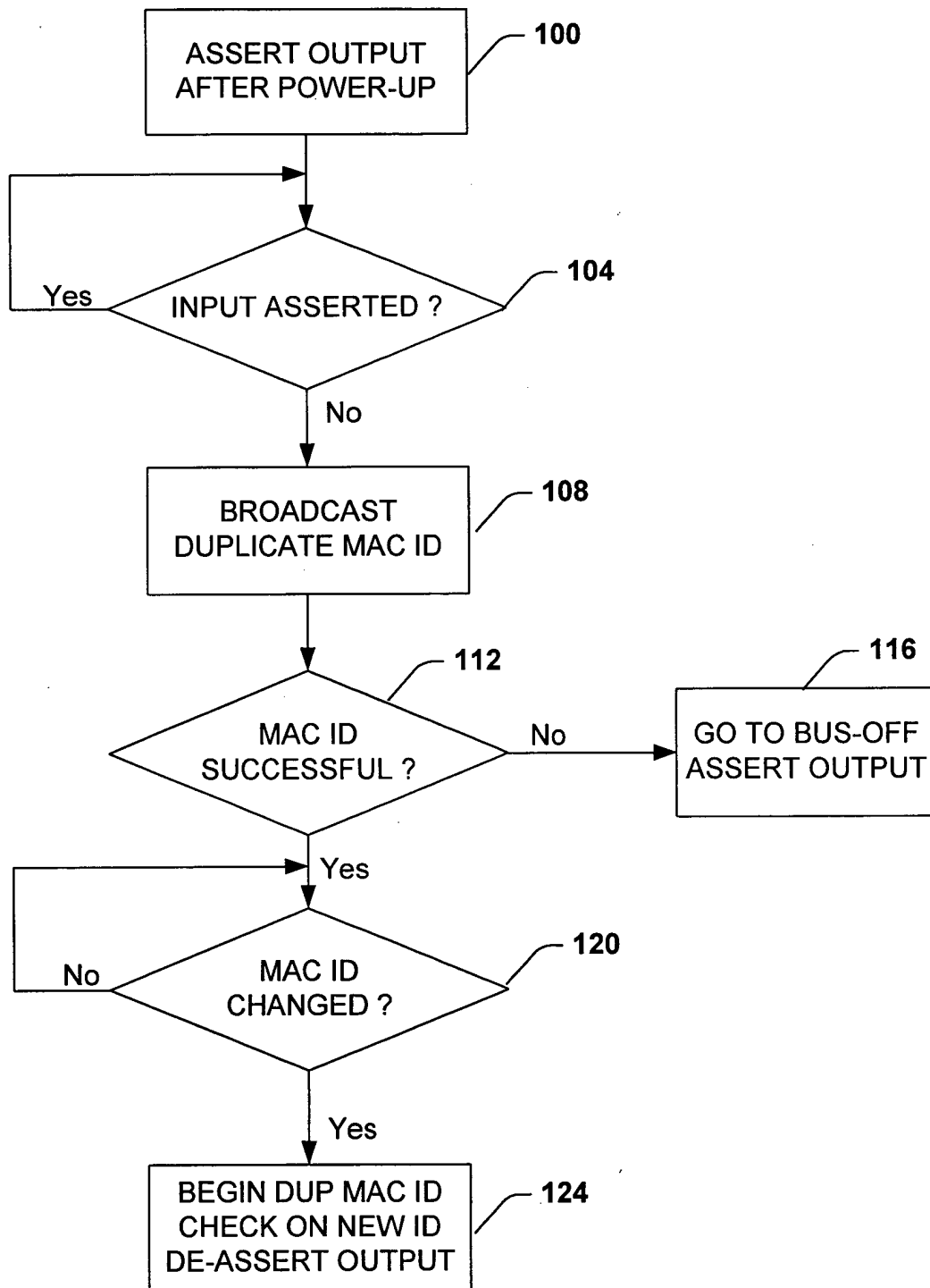
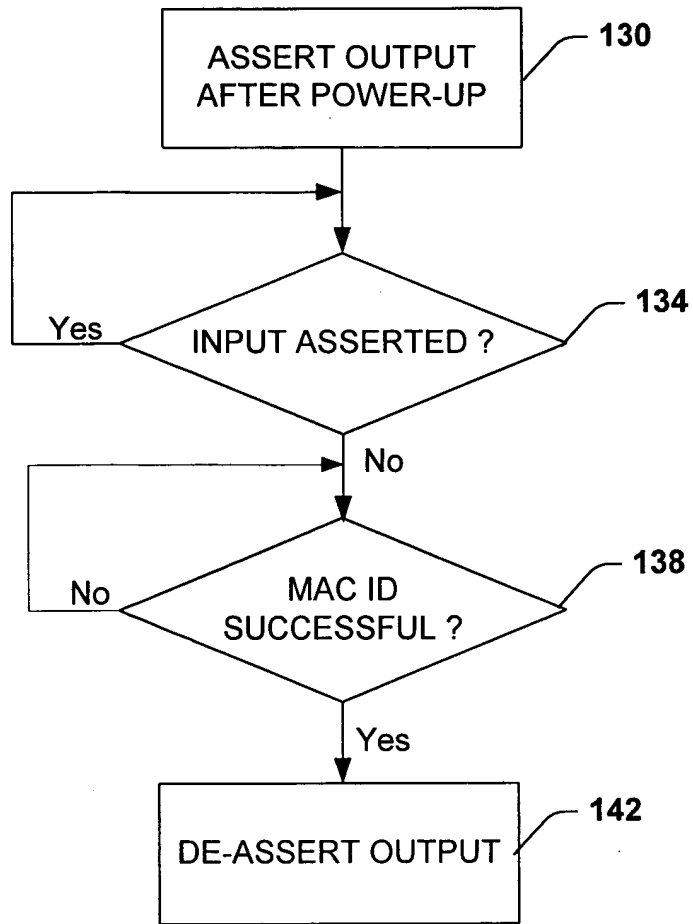


Fig. 2a



**Fig. 2b**



**Fig. 2c**

FIG. 3 is a block diagram of a device 20.

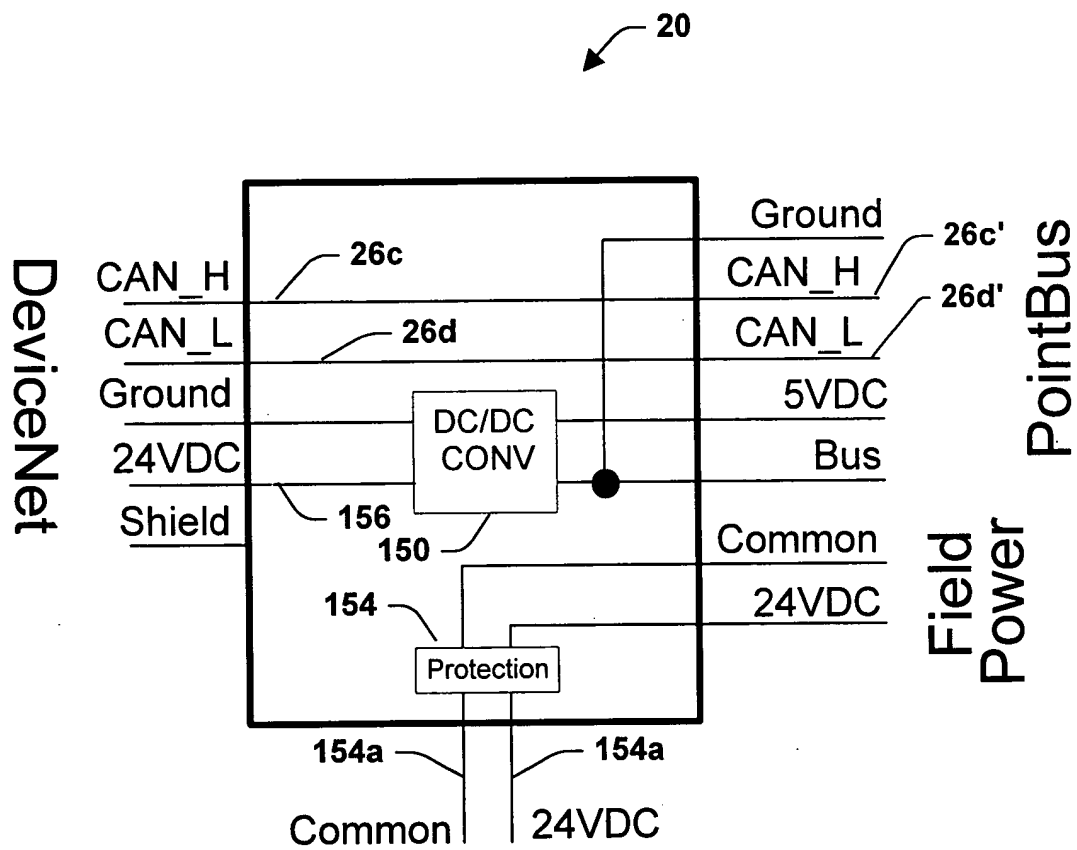


Fig. 3

DeviceNet

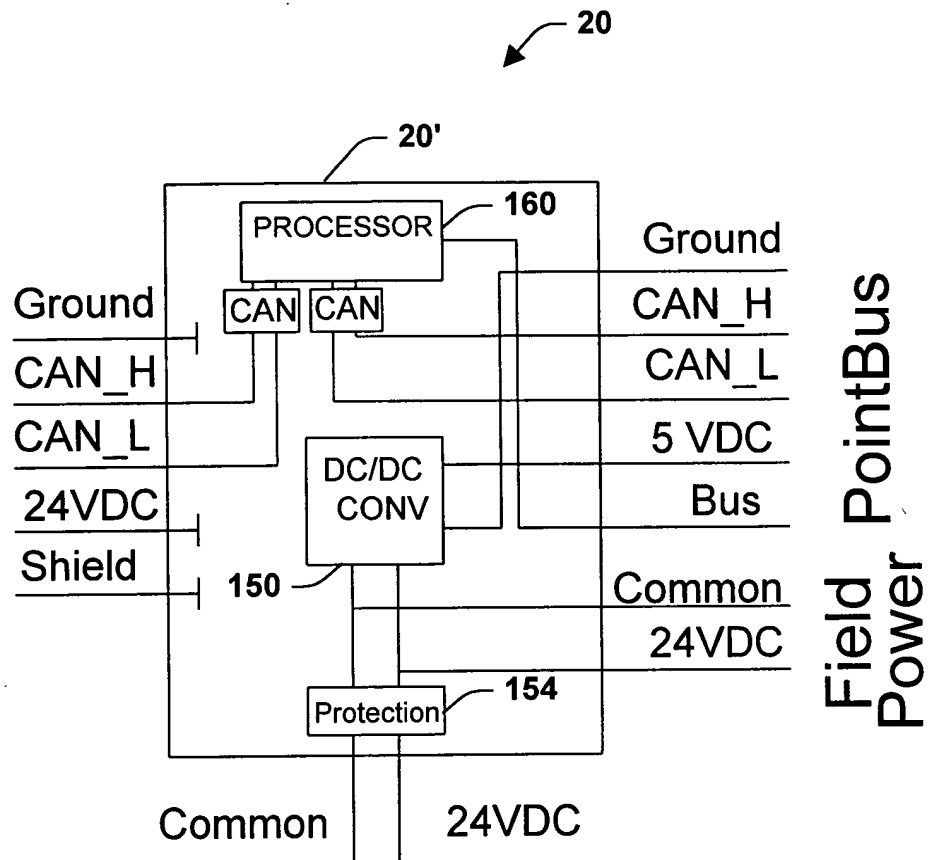
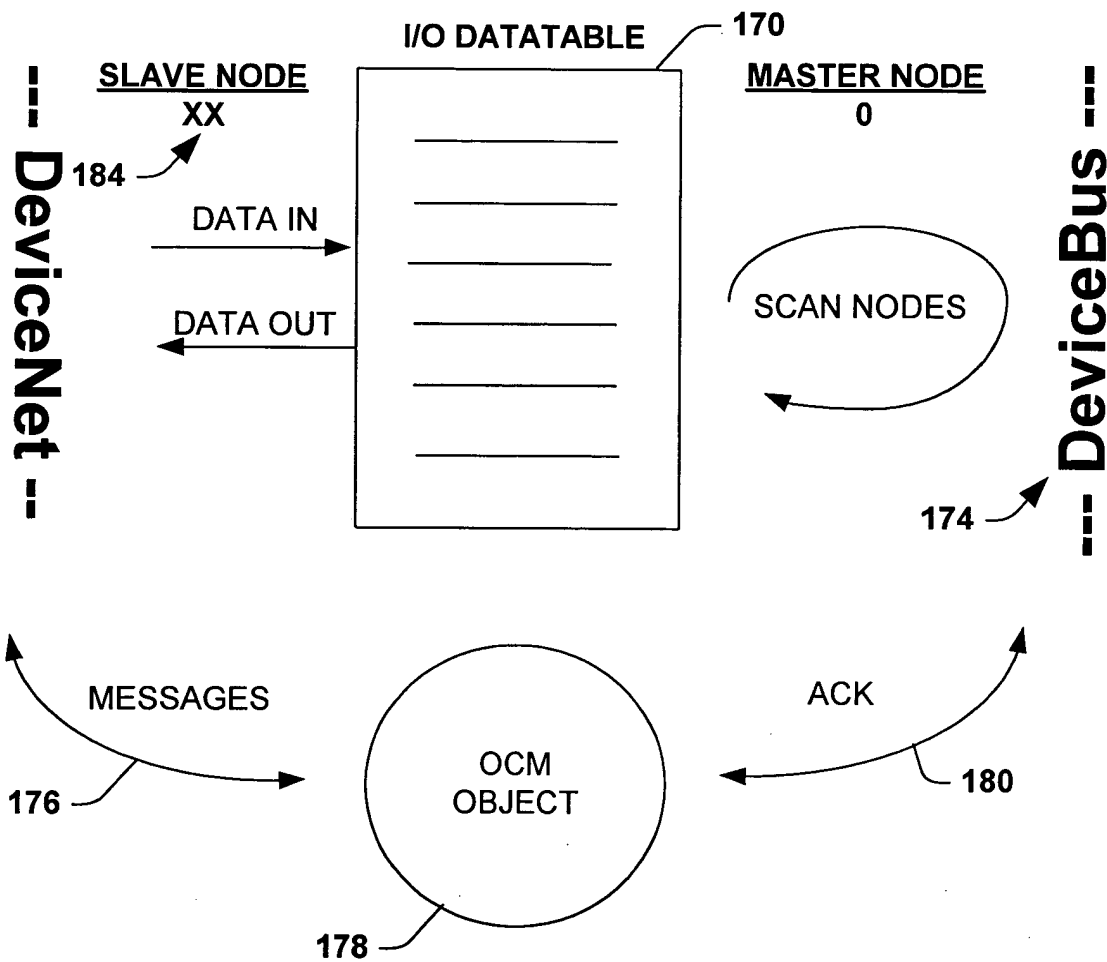
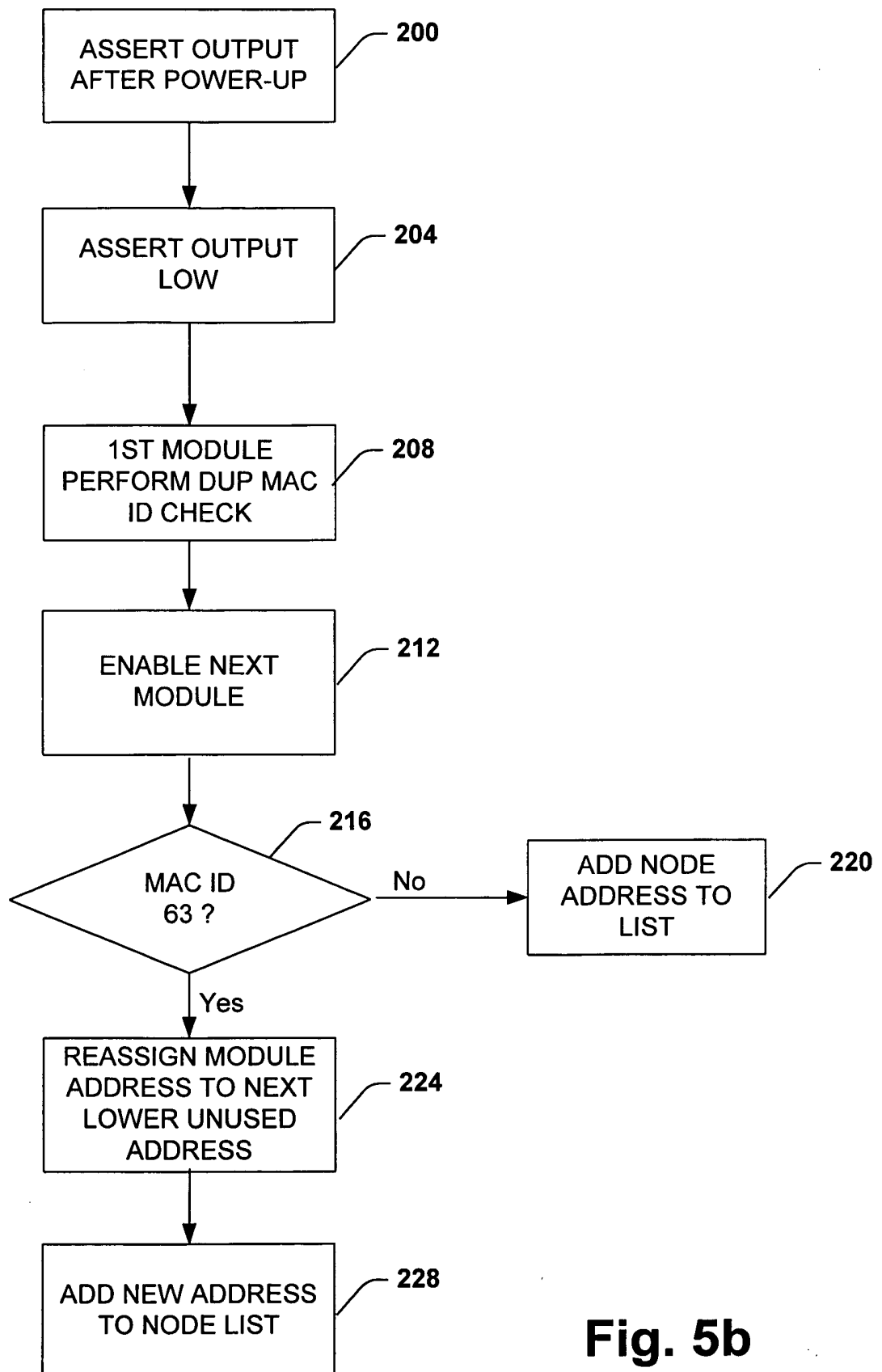


Fig. 4

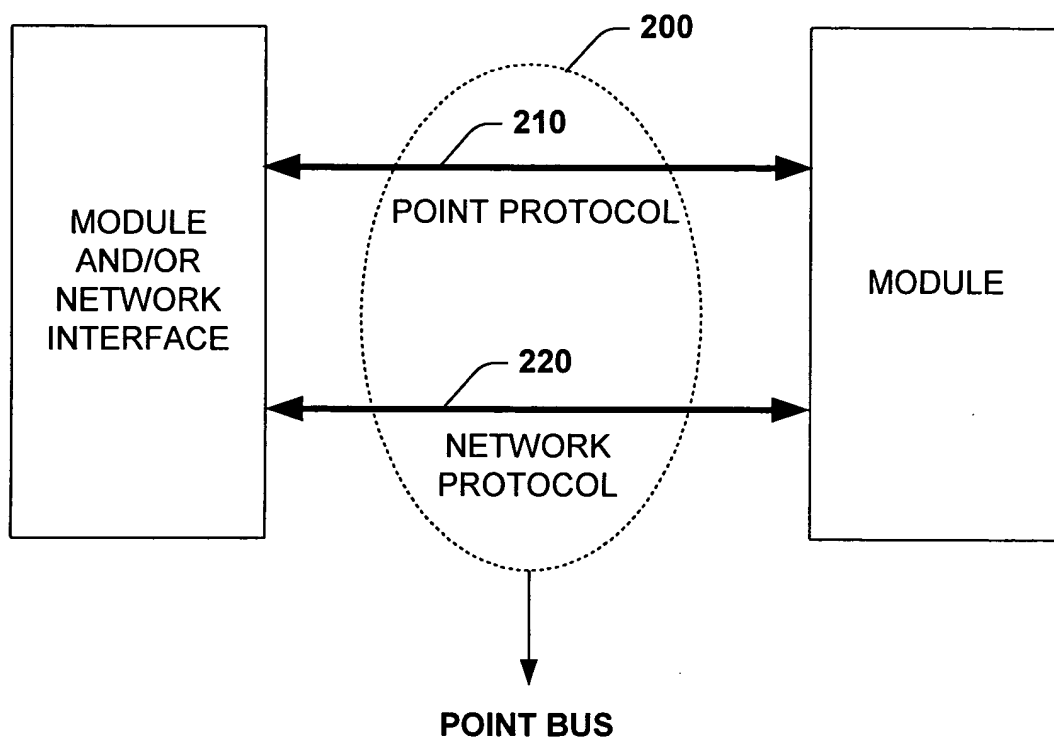


**Fig. 5a**



**Fig. 5b**





**Fig. 6**